

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L18	0	(circuit with reduced near3 harmonic near3 content near3 output and modulated near3 according near3 input and comprising near3 oscillator and one near3 circuit near3 portion near3 configured near3 receive near3 first near3 second near3 orthogonal near3 signals with phases and plural\$3 near3 oscillator near3 outputs near3 respective near3 mutually near3 distinct near3 phases and harmonic near3 rejection near3 mixer and oscillator near3 output near3 respectively near3 weighted with constituting near3 reduced near3 harmonic near3 content near3 output and an near3 array near3 load near3 equalization near3 buffers near3 configured with weight same accordance near3 strength near3 legs near3 forming near3 harmonic near3 rejection near3 mixer and strength near3 legs with determined near3 how near3 much near3 each near3 leg near3 contribut\$4).clm.	US-PGPUB	OR	ON	2006/11/06 13:36